



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No. : 10/667,035  
Applicant : David K. Johansen  
Filed : September 22, 2003  
Title : Multiple Probe Power Systems And Methods For Ultrasonic Welding

TC/A.U. : 1725  
Examiner : Unassigned

Docket No. : 62519-00003USPT  
Customer No. : 30,223

INFORMATION DISCLOSURE STATEMENT  
UNDER 37 C.F.R. §§ 1.97 and 1.98

Mail Stop Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING

I hereby certify that this paper or fee is being deposited with the United States Postal Service on the date indicated above and is addressed to: Mail Stop Patent Application, COMMISSIONER FOR PATENTS, P.O. BOX 1450, ALEXANDRIA, VIRGINIA 22313-1450.

2/17/2004

Adrienne White

Dear Commissioner:

In compliance with the duty of disclosure under 37 C.F.R. § 1.56, it is respectfully requested that this Information Disclosure Statement be entered and the references listed on attached Form PTO-1449 be considered by the Examiner and made of record.

In accordance with the waiver of 37 CFR § 1.98 (a)(2)(i) for submitting a copy of each cited U.S. patent and each U.S. patent application publication for all U.S. national patent applications filed after June 30, 2003, copies of the U.S. patents and patent applications listed on the accompanying Form PTO-1449 are not enclosed.

In accordance with 37 C.F.R. §§ 1.97(g),(h), this Information Disclosure Statement is not to be construed as a representation that a search has been made, and is not to be construed or an

admission that the information disclosed is, or is considered to be, prior art to the present application or material to patentability as defined in 37 C.F.R. § 1.56.

The present Information Disclosure Statement is being filed prior to the receipt of a first Official Action reflecting an examination on the merits and is timely in accordance with 37 C.F.R. § 1.97(b). Should any fees be deemed necessary, the Commissioner is hereby authorized to charge any fees due while this application is pending (except the issue fee) to Jenkins & Gilchrist Deposit Account No. 10-0447 (62519-00003USPT).

Respectfully submitted,

Date: February 17, 2004



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Supplements and Publications for Applicant's  
**INFORMATION DISCLOSURE STATEMENT**

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Page 1 of 1

Applicant  
David K. JohansenFiling Date:  
September 22, 2003

Group: 1725

**U.S. Patent Documents**

Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date if App.
	A01	4,064,462	12/20/1977	Goehler et al.			
	A02	4,131,505	12/26/1978	Davis, Jr.			
	A03	4,208,001	06/17/1980	Martner			
	A04	4,257,730	03/24/1981	Kawakita			
	A05	4,277,710	07/07/1981	Harwood et al.			
	A06	4,838,639	06/13/1989	Morankar et al.			
	A07	4,914,290	04/03/1990	Hilgart et al.			
	A08	5,295,700	03/22/1994	Crews et al.			
	A09	5,798,599	08/25/1998	Harwood			
	A10	5,880,580	03/09/1999	Johansen			

**Foreign Patent Documents**

Exam. Init.	Ref. Des.	Document Number	Date	Country	Class	Sub Class	Translation Yes/No

**Other Art (Including Author, Title, Date Pertinent Pages, Etc.)**

Exam. Init.	Ref. Des.	Citation
		"Hex Buffers/Logic-Level Down Converters High-Performance Silicon-Gate CMOS," Motorola Semiconductor Technical Data, Motorola, Inc. 1995, 6 pages.

**EXAMINER:****DATE CONSIDERED:**

EXAMINER: INITIAL IF REFERENCE CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED. INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.